

EAST - [Untitled1:1]

File View Edit Tools Window Help

Drafts  
Pending  
Active  
L1: (6) ((processor near5 gr  
Failed  
Saved  
Favorites  
Tagged (0)  
UDC  
Queue  
Trash

Search List Browse Save Clear  
DBs: USPAT  
Default operator: OR  
Plurals  
Highlight all hit terms initially

BRS I... IS&R... Image Text HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Err
1	BRS	L1	6	((processor near5 group) same crossbar\$1) and	USPAT	2003/12/31 12:49			0

Start EAST - [Untitled1...

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Search

DBs:  ☒ Plurals  
Default operator:  ☒ Highlight all hit terms initially

((processor near5 group) same crossbar\$1) and ((memory near5 group) same crossbar)

BRS I... IS&R... Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	R
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6262594 B1	20010717	38	Apparatus and method for configurable use of groups	326/38	326/41	
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6219627 B1	20010417	10	Architecture of a chip having multiple processors	703/1	29/827	
3	<input type="checkbox"/>	<input type="checkbox"/>	US 5701507 A	19971223	11	Architecture of a chip having multiple processors	716/8	438/108; 438/129;	
4	<input type="checkbox"/>	<input type="checkbox"/>	US 5583506 A	19961210	257	Signal processing system and method	342/25	700/3	
5	<input type="checkbox"/>	<input type="checkbox"/>	US 5226125 A	19930706	71	Switch matrix having integrated crosspoint logic	710/317	340/2.28	
6	<input type="checkbox"/>	<input type="checkbox"/>	US 4852083 A	19890725	10	Digital crossbar switch	370/381	340/2.28; 340/2.71;	

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